



Curriculum Vitae of a faculty member

1. Personal information

Name	Dr. Rafid Ahmed Khalil Alamori
Academic Degree	Assist. Prof.
Job Title	
General Major	Electronics Eng.
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2. Qualifications

Degree	Date of Graduation	Name of university	Country	Major
Doctorate	1997	University of Technology	Iraq	Electronics Eng.
Master	1991	University of Mosul	Iraq	Electronics & Communication Eng.
Bachelor	1983	University of Mosul	Iraq	Electrical Eng.

3. Researches & Scientific activities

Journal and Conference Papers

1. Rafid A. Khalil, Basil M. Saied, Rakan Kh. Antar , "Power Quality Improvement of High Voltage DC Link using Modified Shunt Active Power Filter", 1st International Scientific Conference in Cooperation with LUND University-Sweeden, Al-Kitab University College, Baghdad, Iraq, December, 2017.
2. Rafid A. Khalil ,Rakan Kh. Antar, Basil M. Saied, "Power Quality Improvement in a HVDC Transmission System Based on a Modified Active Power Filter", Al-Rafidain Engineering Journal, University of Mosul, College of Engineering, ISSN: 18130526, vol. 21, Issue 5, pages: 16-24, 2013.
3. Rafid A. Khalil ,Mustafa Salem, "Hybrid labview and labview FPGA implementation of simple objects classification system", the 2nd scientific engineering conf., Eng. college, University of Mosul, 2013.
4. Rafid A. Khalil, Basil M. Saied, and Rakan Kh. Antar, "Modified Active Power Filter Based on Five-level Cascade H-Bridge PWM Voltage Source Inverter", the 9th International Conference on Electronics, Computer and Computation, ICECCO2012, Turgut Ozel University, Ankara, Turkey, November 2012.
www.icecco.org
5. Rafid A. Khalil ,Mustafa Salem, "Enhanced Hardware implementation of Hybrid Stochastic ANN using FPGA", Al-Rafidain Engineering Journal, University of Mosul, College of Engineering, ISSN: 18130526, vol 22, (2), 2014.
6. Rafid A. Khalil ,Mustafa Salem, "Hybrid labview and labview FPGA implementation of simple objects classification system" Al-Rafidain Engineering Journal, University of Mosul, College of Engineering, ISSN: 18130526, vol 22, (2), 2014.
7. Rafid A. Khalil ,Mustafa Salem, "Enhanced Hardware implementation of Hybrid Stochastic ANN using FPGA", the 2nd scientific engineering conf., Eng. college, University of Mosul, 2013.
8. Rafid A. Khalil ,Rakan Kh. Antar, "Neural Network Based on Model Reference using for robot arm identification and control", the 2nd scientific engineering conf., Eng. college, University of Mosul, 2013.
9. Rafid A. Khalil, Rakan Kh. Antar, Basil M. Saied, and Ghanim A. Putrus, "HVDC Link Power Quality Improvement using A Modified Active Power Filter", 47th International Universities' Power Engineering Conference (UPEC2012), Brunel

- Institute of Power Systems, Brunel University, IEEE IET Conference, pages: 1-5, London, UK, 2012. (Scopus).
10. Rafid A. Khalil ,Rakan Kh. Antar, "Neural Network Based on Model Reference using for robot arm identification and control", Al-Rafidain Engineering Journal, University of Mosul, College of Engineering, ISSN: 18130526, vol 22, (2), 2014.
 11. Rafid A. Khalil, " Synthesis of embedded VHDL design: of Artificial Neurons with different computation models", the 3rd Conference For Engineering Sciences, University of Tikret, Iraq, December 2012.
 12. Rafid A. Khalil, Aws H. al_Anaz, "FPGA Implementation of Adaptive Noise Cancellor", Al-Rafiden Engineering Journal, vol. 17 , no. 4, ISSN: 1813-0526, 2009.
 13. Rafid A. Khalil, " FPGA Implementation of Multilayer Perceptron for Speech Recognition" ,The 2st Conference For Applied Sciences, University of Duhook, Duhook, Iraq, October 2012.
 14. Rafid A. Khalil, Aws H. al-Anaz, " Design and Implementation of Artificial Neurons with different computation models", Philadelphia Eng. Conference, Philadelphia University, Amman, Jordan, 2012.
 15. Rafid A. Khalil, " Towards an FPGA Based Reconfigurable Computing Environment for MLP Implementations", The 1st international Eng. Sciences Conference IESC ' 2008, University of Aleppo, November 2-4, 2008, Aleppo, Syria.
 16. Rakan Kh. Antar, Basil M. Saied, and Rafid A. Khalil, "Using Seven-Level Cascade H-Bridge Inverter with HVDC System to Improve Power Quality", The 1st National Conference For Engineering Sciences, FNCES'12, University of Al-Nahrain, Baghdad, Iraq, November 2012, (Scopus)
 17. Rayan S. Gargees, Rafid A. Khalil, " DC Motor Speed Control Using TMS320F2812 Processor ", IEEE International Conf. on Electric Power and Energy Conversion Systems, EPECS' 09, American University of Sharjah, UAE, November 10-12, 2009.
 18. Rafid A. Khalil, "Reconfigurable Implementation of MLP for Speech Recognition", The 3rd IEEE ICTTA' 08, 7-11 April 2008, Damascus, Syria. (Publisher: IEEE Explorer)
 19. Rafid A. Khalil, " Dynamic Neural Network Based on Genetic Algorithm for Nonlinear System Identification", Global Conf. on Renewables and Energy Efficiency for Desert Regions GCREEDER 2011, Amman, Jordan.

20. Rafid A. Khalil, Saad A. Alkazzaz" FPGA Implementation of Artificial Neurons: Comparison Study", The 3rd IEEE ICTTA' 08, 7-11 April 2008, Damascus, Syria. (Publisher: IEEE Explorer)
21. Rafid A. Khalil, " System on Chip (SoC) Adaptive Filtering Based FPGA", The 9th Conf. of Arab Engineering Colleges, Damascus University, April 13-14, 2009.
22. Rayan S. Gargees, Rafid A. Khalil, " Digital Signal Processor TMS 320F2812 Based Real-Time DC Motor Speed Control", The 2nd Alzarqa Engineering Science Conf., 24-26, November 2010, AlZarqa Private University, Zarqa, Jordan.
23. Rayan S. Gargees, Rafid A. Khalil, " DSP Based Adjustable Closed Loop DC Motor Speed Control", Al-Rafidain Engineering Journal, University of Mosul, College of Engineering, ISSN: 18130526, 19 (5), 2011.
24. Rayan S. Gargees, Rafid A. Khalil, " Real-Time DC Motor Speed Control Using TMS320F2812 DSP Processor ", The 1st international Eng. Sciences Conference IESC ' 2008, University of Aleppo, November 2-4, 2008, Aleppo, Syria.
25. Rafid A. Khalil, Aws H. al-Anaz, " An Embedded System on Chip (SoC) Adaptive Filtering Based FPGA", The 1st international Eng. Sciences Conference IESC ' 2008, University of Aleppo, November 2-4, 2008, Aleppo, Syria.
26. Rafid Ahmed Khalil, "Learning Methods Based on Genetic Algorithm for Nonlinear Dynamic Systems Identifications", The 5th International Conf. on Electrical Eng. CEE' 2008, 27-29 October, 2008, University of Batna, Algeria. (Publisher: IEEE Explorer)
27. Rafid A. Khalil," Hardware Implementation of Backpropagation Neural Networks on Field programmable Gate Array (FPGA)", The First Electrical Engineering Conference (EEC-2007), Aleppo University, Aleppo, Syria, 26-28, 2007.
28. Rafid A. Khalil, " Adaptive Filter Application in Echo Cancellation System and Implementation using FPGA", Al-Rafiden Engineering Journal, vol. , no. , , pp. , ISSN: 1813-0526.
29. Rafid A. Khalil, Nour T. Gadawe, " FPGA Implementation of a Multilayer Perceptron (MLP) Network", Al-Rafiden Engineering Journal, vol. 17 , no. 1 , 2009, ISSN: 1813-0526.
30. Rafid Ahmed Khalil, " Comparison of four Neural Network Learning Methods Based on Genetic Algorithm for Nonlinear Dynamic Systems Identifications", Al-Rafiden Engineering Journal, vol. 20 , no. 1 , 2012, ISSN: 1813-0526.

31. Rafid A. Khalil," Digital Image Compression Enhancement Using Bipolar Backpropagation Neural Networks", Al-Rafiden Engineering Journal, vol. 15, no. 4, 2007, pp. 40-52, ISSN: 1813-0526, 2007.
32. Rafid A. Khalil, Mohammed C. Younis, " Image Compression Technique Using a Hierarchical Neural Network" Al-Rafiden Science and Mathematics Journal, vol. 3, no. 2, 2006, pp. 99-112, ISSN: 1815-4516.
33. Rafid A. Khalil, Sa'ad A. Al-Kazzaz, " Digital Hardware Implementation of Artificial Neurons Models Using FPGA", Al-Rafiden Engineering Journal, vol.17 , no.2 , 2007, ISSN: 1813-0526.
34. Rafid A. Khalil, "Hardware Implementation of Backpropagation Neural Networks on Field programmable Gate Array (FPGA)", Al-Rafiden Engineering Journal, 16 (3), 2008 , ISSN: 1813-0526.
35. Rafid A. Khalil, " Handwritten Arabic Alphanumeric Character Recognition using BP and SOFM Neural Networks", Al-Rafiden Engineering Journal, vol. 14, no. 2, 2006, pp. 68-80, ISSN: 1813-0526.
36. Rafid A. Khalil, " Recognition of Handwritten Arabic Alphanumeric Characters by Backpropagation Neural Networks", Prospective Research Journal, vol. 14, No.4, 2006, pp. 45-60, ISSN: 1680- 9300.
37. Rafid A. Khalil, Rakan Kh. Antar, and Basil M. Saied, "HVDC Link Power Quality Improvement Based on Active and Passive Filters", The 2nd international Eng. Sciences Conference IESC ' 2011, University of Aleppo, May 24-26, 2011, Aleppo, Syria.
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39. Rafid A. Khalil, Sa'ad A. Al-Kazzaz, Zead Mohammed Yosif, " Using LEGO/ LabVIEW for Robotic Lab-Work", UN ESCWA Technology Center (ETC), NI Academic, National Instruments, May 16-19, 2012, Beirut- Lebanon.
40. Rafid A. Khalil, " Hybrid Stochastic Neural Network Implementation Using FPGA" Journal of Electrical and Electronics Engineering Research, Accepted: Feb 2nd, 2018.
41. Rakan Kh. Antar, Basil M. Saied, and Rafid Ahmed Khalil, " Using Seven Level Cascade H-Bridge Inverter with HVDC System to Improve Power Quality", The 1st national Conference for Engineering Sciences , 2012. (Scopus).